



INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
				Application No.	09/748,098
				Filing Date	December 21, 2000
				First Named Inventor	Nicholas J. Kelsey
				Art Unit	2183
Examiner Name	Charles A. Harkness				
Sheet	1	of	1	Attorney Docket Number	20880-05093

U.S. PATENT DOCUMENTS				
Examiner Initials*	Cite No. ¹	Document No. Number - Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
007H	A1	US-5,515,538	05-07-1996	Kleiman
007H	A2	US-5,524,250	06-04-1996	Chesson et al.
007H	A3	US-5,727,211	03-10-1998	Gulsen

FOREIGN PATENT DOCUMENTS					
		Foreign Patent Document			
Examiner Initials*	Cite No. ¹	Country Code ² – Number ³ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶
007H	B1	WO 99/54813	10-28-1999	Transwitch Corp.	

OTHER REFERENCES – NON-PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ⁶
DOY	C1	EL-KHARASHI, et al., Multithreaded Processors: The Upcoming Generation for Multimedia Chips, 1998 IEEE Symposium on Advances in Digital Filtering and Signal Processing, June 5-6, 1998, pp. 111-115			

Examiner Signature		Date Considered	4-6-05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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